FIG. 1

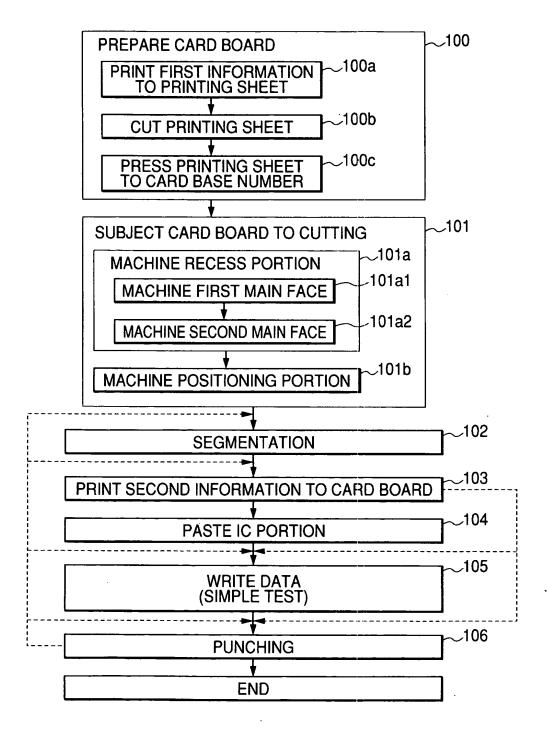


FIG. 2

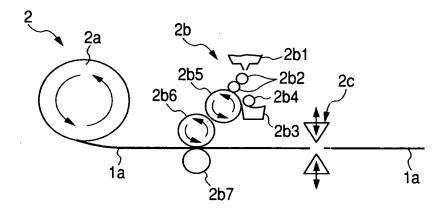


FIG. 3

1c
1a
1a
1b
1a, 1c
1b
1a, 1c

FIG. 4

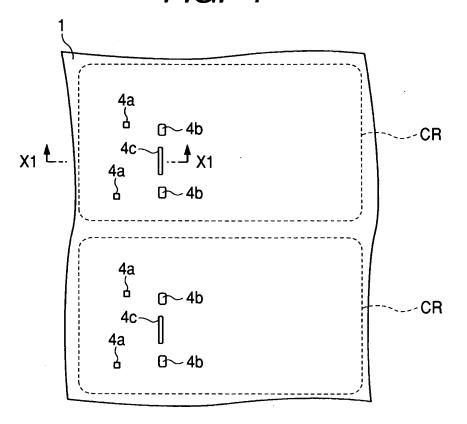


FIG. 5

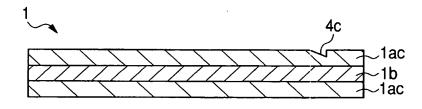


FIG. 6

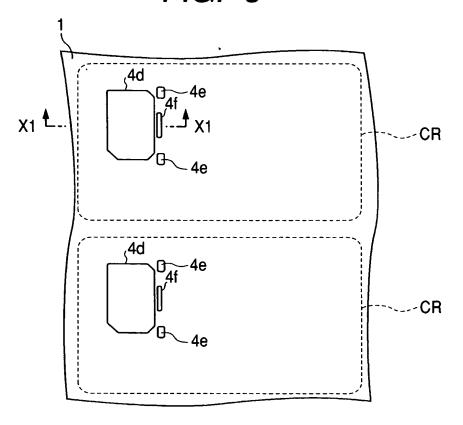


FIG. 7

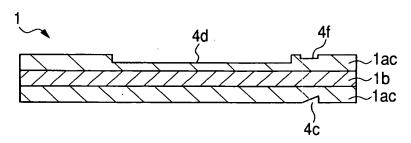


FIG. 8

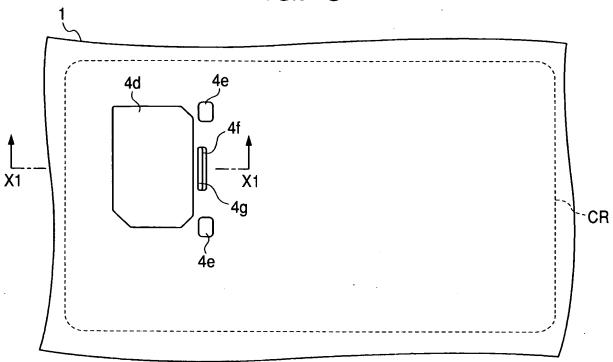


FIG. 9

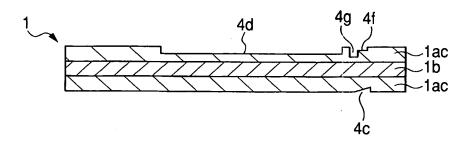


FIG. 10

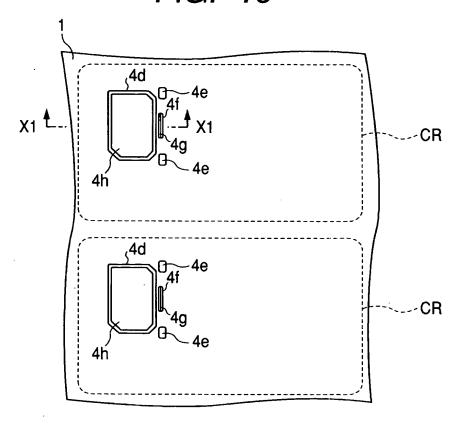
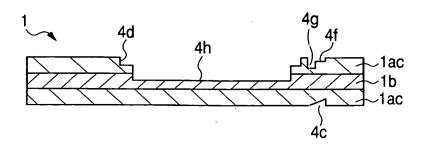


FIG. 11



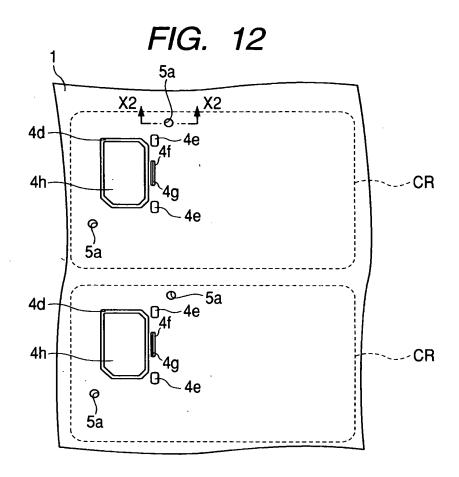
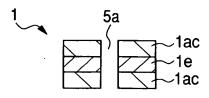


FIG. 13



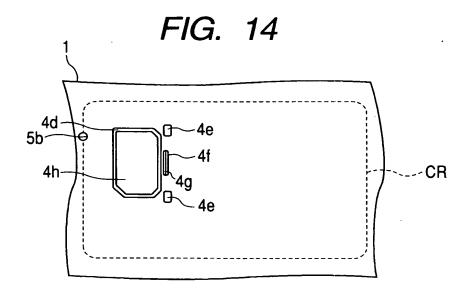


FIG. 15

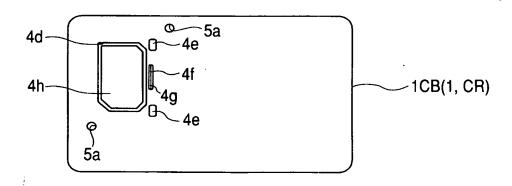


FIG. 16

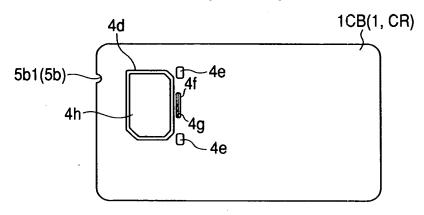


FIG. 17

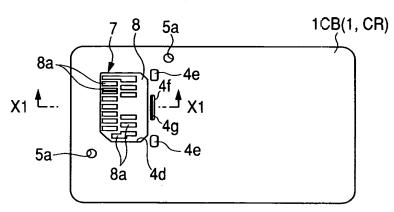


FIG. 18

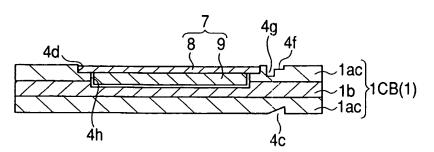
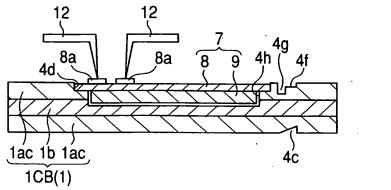


FIG. 19



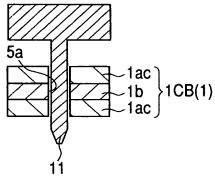
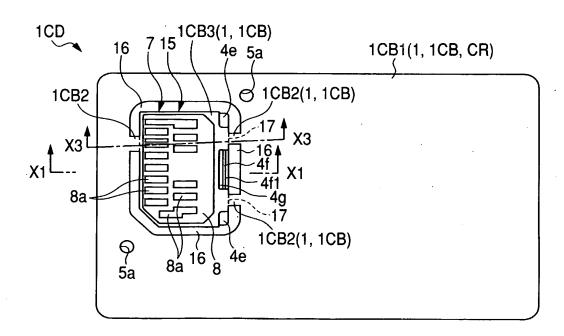


FIG. 20



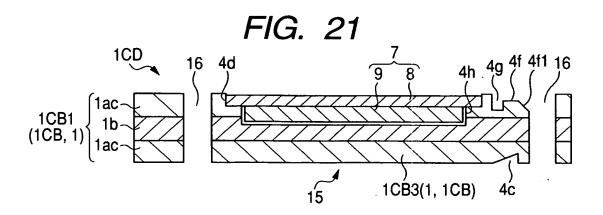


FIG. 22

1CD 1CB2 17 4d 4h 9 8 17 1CB2 17 1CB2 17 1CB2 17 1CB2 17 1CB3(1, 1CB) 17 1CB3(1, 1CB) 17

FIG. 23

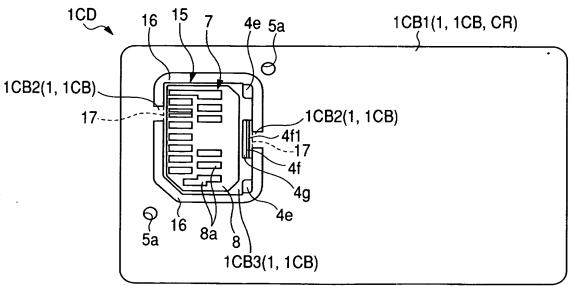


FIG. 24

1CD

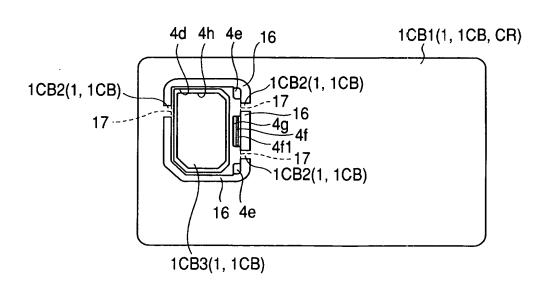
15 16 7 4e

1CB2
(1, 1CB)

1CB2
(1, 1CB)

4g
4f
4f1
16
16
1CB2
(1, 1CB)
1CB3
(1, 1CB)
1CB3
(1, 1CB)

FIG. 25



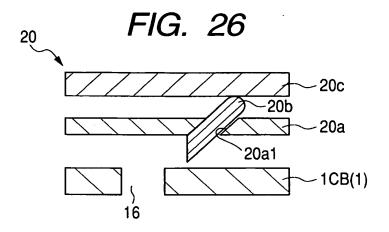


FIG. 27

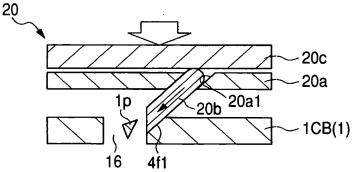


FIG. 28

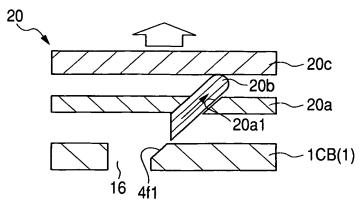
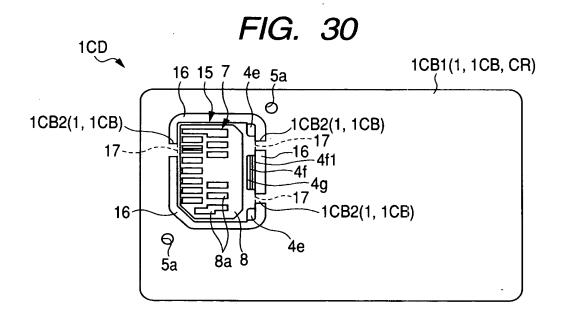


FIG. 29 1CD 1CB1(1, 1CB, CR) 15 4b 4a 5a --1CB2(1, 1CB) 16 -4c 16 1CB2(1, 1CB)-17 1CB2(1, 1CB) 17---16 O_5a 4a 1ĆB3(1, 1CB)



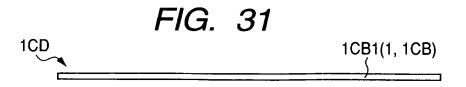


FIG. 32

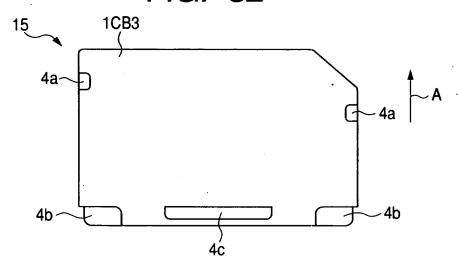


FIG. 33

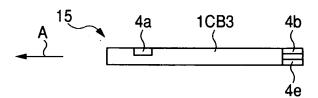
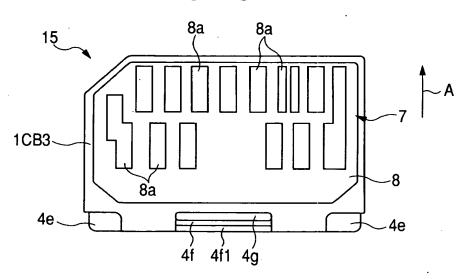
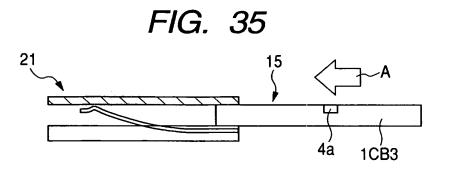


FIG. 34





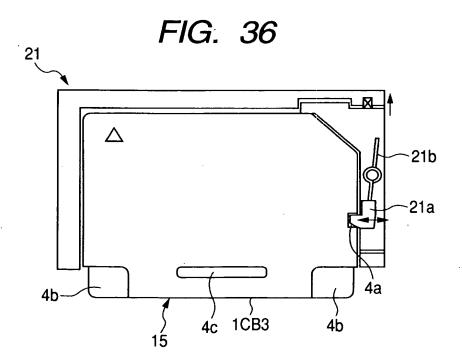


FIG. 37

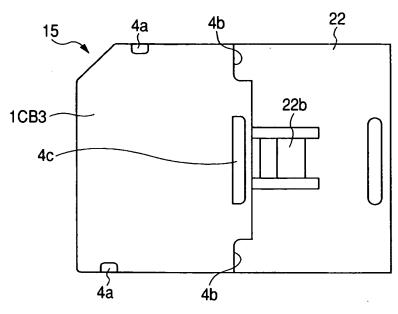


FIG. 38

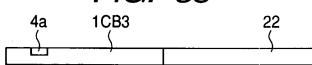


FIG. 39

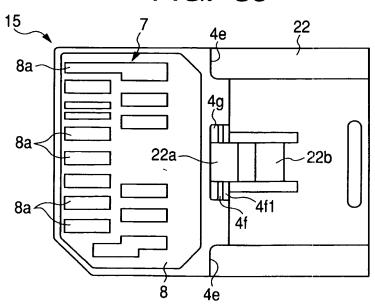


FIG. 40

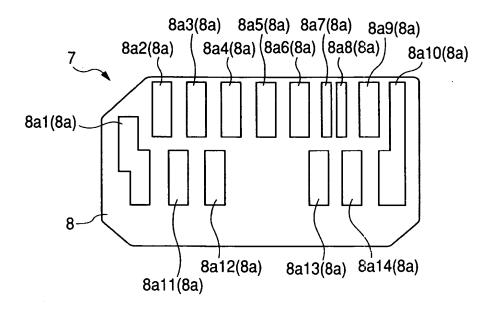


FIG. 41

FIG. 42

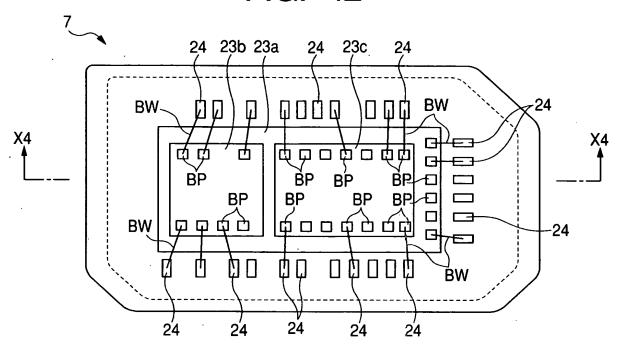
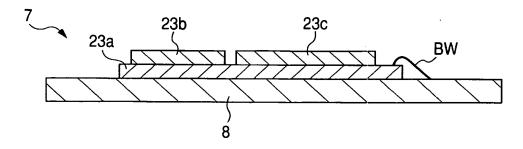


FIG. 43



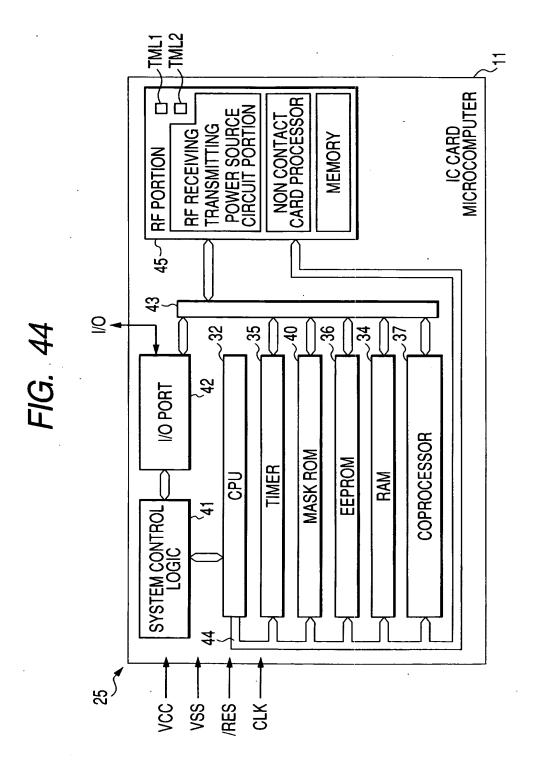


FIG. 45

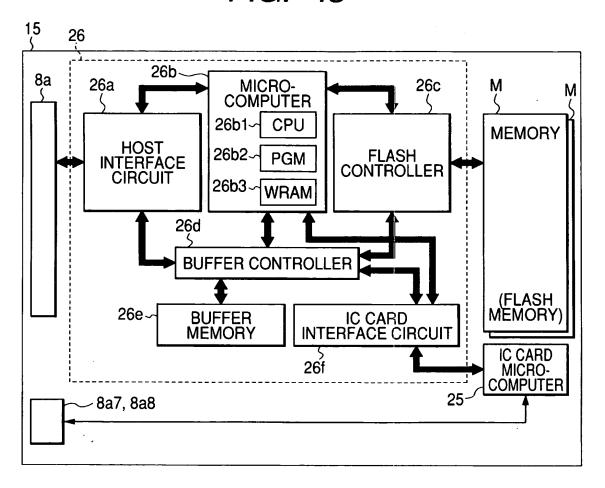


FIG. 46

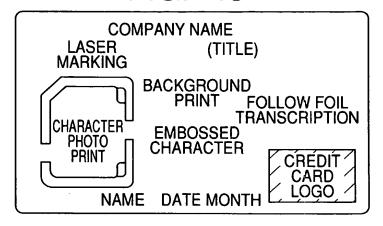


FIG. 47

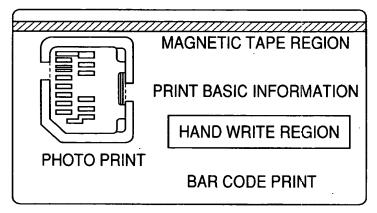


FIG. 48

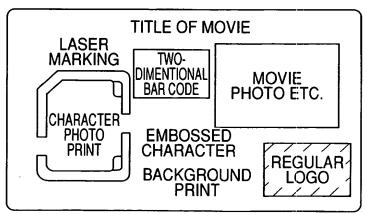


FIG. 49

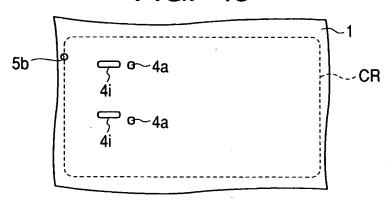


FIG. 50

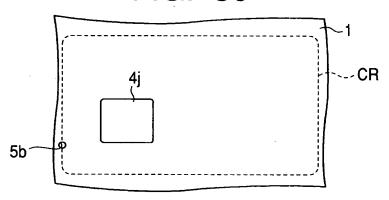


FIG. 51

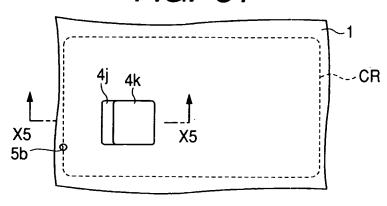


FIG. 52

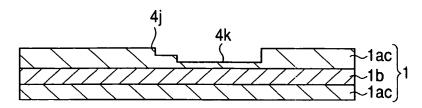


FIG. 53

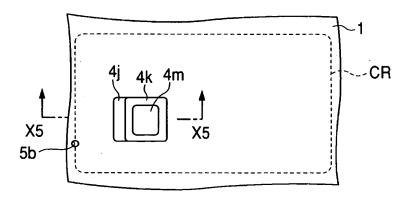


FIG. 54

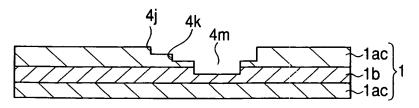


FIG. 55

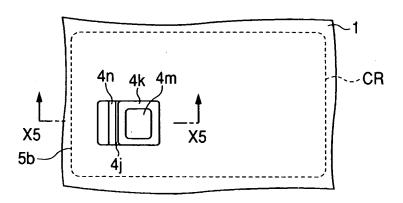


FIG. 56

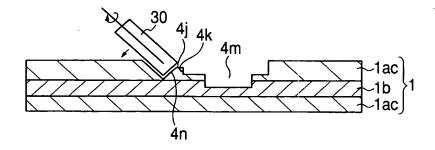


FIG. 57

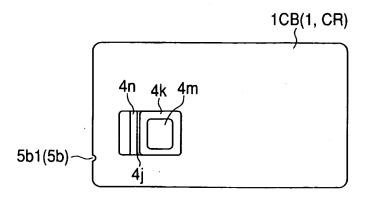


FIG. 58

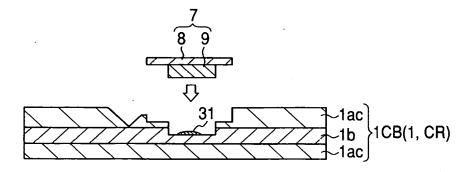


FIG. 59

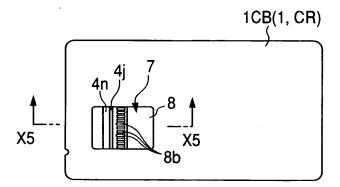


FIG. 60

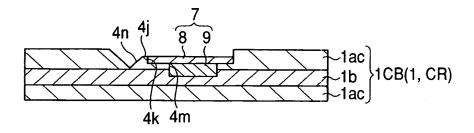


FIG. 61

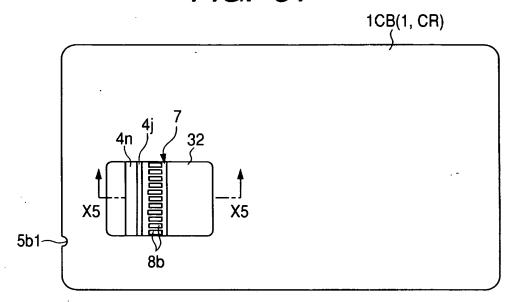


FIG. 62

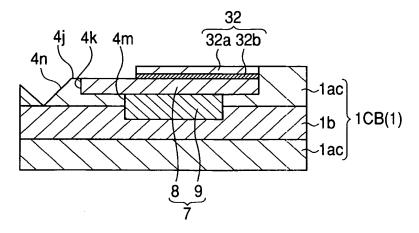


FIG. 63

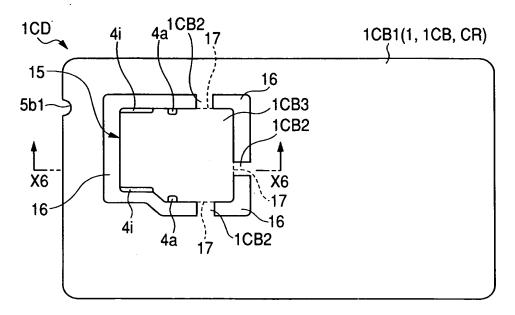


FIG. 64

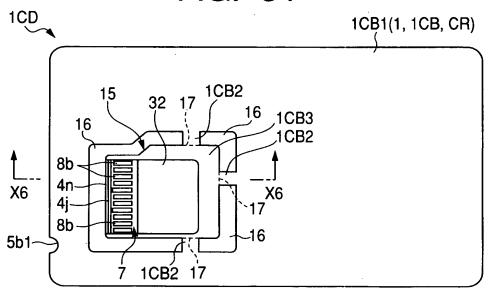


FIG. 65

1CD 1CB1(1, 1CB)

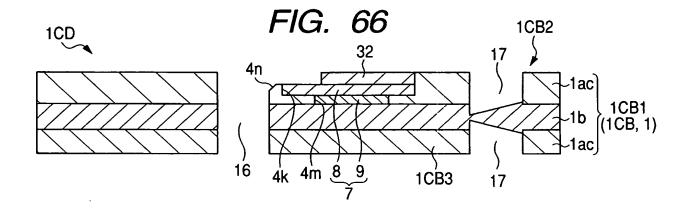


FIG. 67

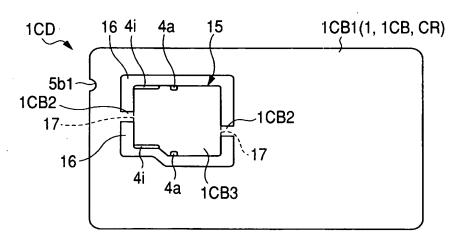


FIG. 68

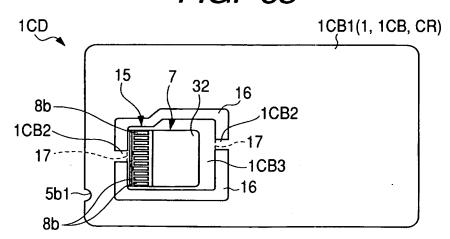


FIG. 69

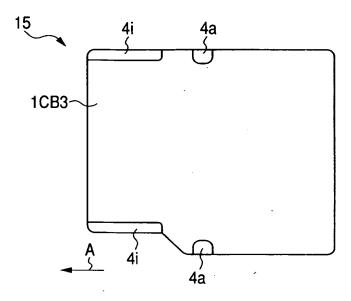


FIG. 70

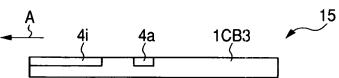


FIG. 71

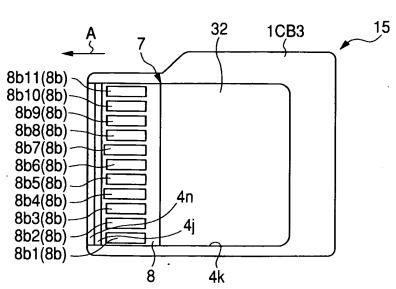


FIG. 72

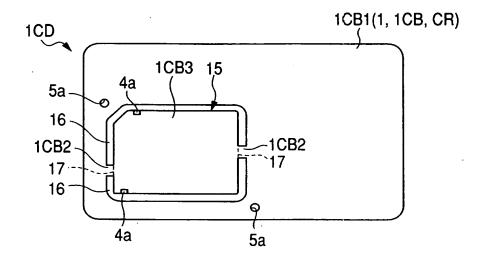


FIG. 73

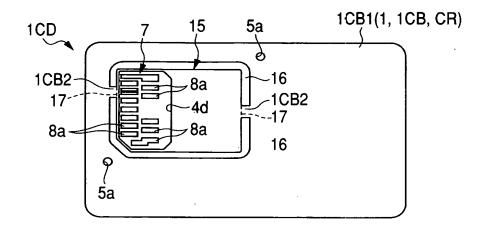


FIG. 74

1CB1(1, 1CB, CR)

1CD

FIG. 75

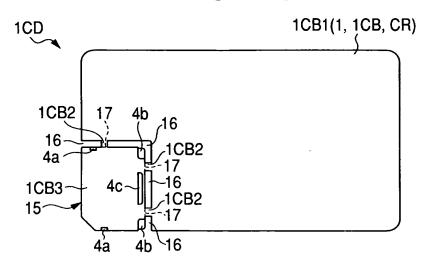


FIG. 76

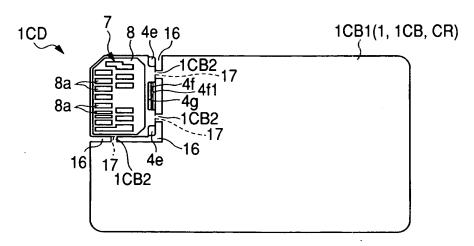


FIG. 77



FIG. 78

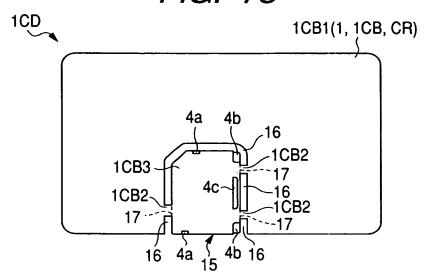


FIG. 79

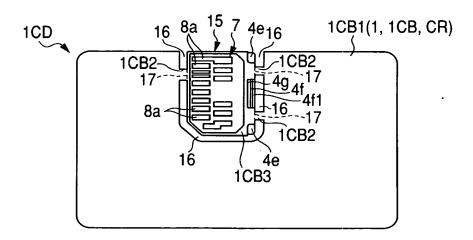


FIG. 80



FIG. 81

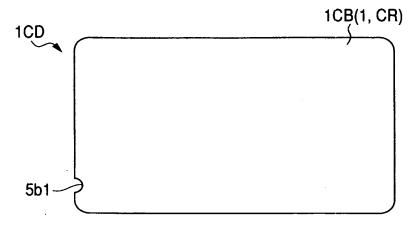


FIG. 82

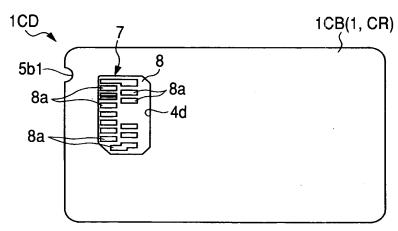


FIG. 83



FIG. 84

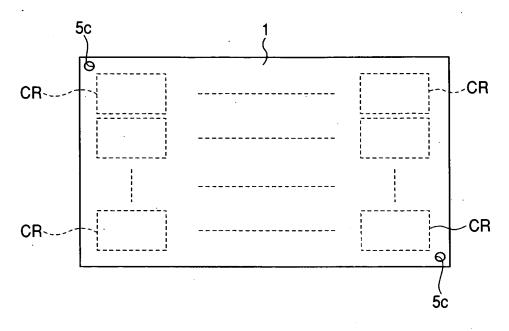
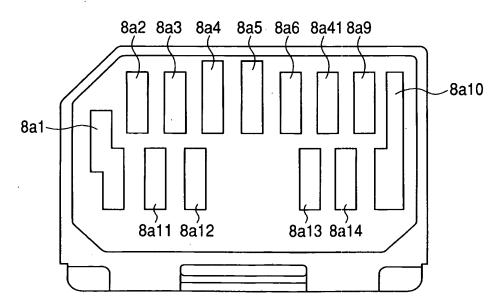


FIG. 85



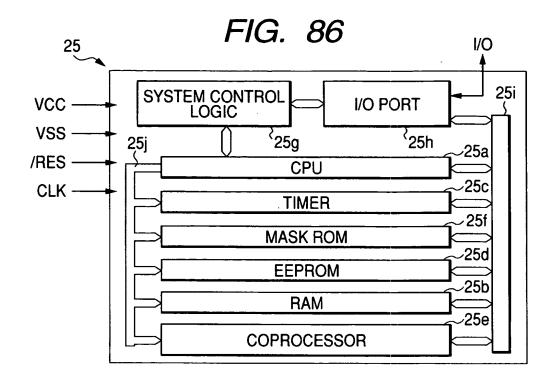


FIG. 87 15 26 26b-8a 26a 26c MICRO-Μ M **COMPUTER** 26b1~ **CPU MEMORY** HOST INTERFACE CIRCUIT FLASH CONTROLLER 26b2~ **PGM** 26b3~ **WRAM** 26d BUFFER CONTROLLER (FLASH MEMORY) 26e~ BUFFER MEMORY IC CARD INTERFACE CIRCUIT IC CARD MICRO-COMPUTER 26f 25

FIG. 88

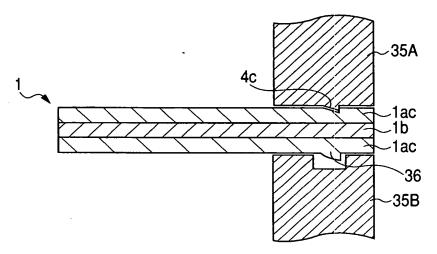


FIG. 89

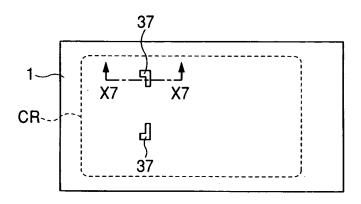


FIG. 90

